SBF5089Z

DC to 500MHz, CASCADABLE InGaP/GaAs HBT MMIC AMPLIFIER



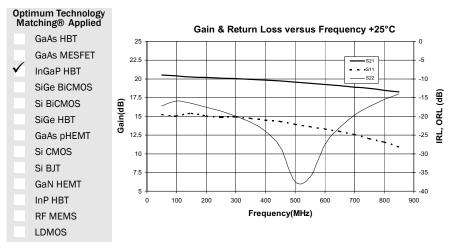
Package: SOT-89



Product Description

rfmd.com

RFMD's SBF5089Z is a high performance InGaP/GaAs Heterojunction Bipolar Transistor MMIC Amplifier. A Darlington configuration designed with InGaP process technology provides broadband performance up to 0.5GHz with excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only a single positive supply voltage, DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.



Features

- IP₃=41dBm at 240MHz
- Stable Gain Over Temperature
- Robust 1000V ESD, Class 1C
- Operates From Single Supply
- Low Thermal Resistance

Applications

- Receiver IF Applications
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite Terminals

Deverseter	Specification			Unit	O omdition
Parameter	Min.	Тур.	Max.	Unit	Condition
Small Signal Gain		20.5		dB	70MHz
	18.5	20.0	21.5	dB	240 MHz
	18.0	19.5	21.0	dB	500MHz
Output Power at 1dB Compression		21		dBm	70MHz
		21		dBm	240 MHz
	19.2	20.7		dBm	400 MHz
Output Third Order Intercept Point		39.0		dBm	70MHz
		41.0		dBm	240 MHz
	37.5	39.5		dBm	400 MHz
Input Return Loss	14	18		dB	500 MHz
Output Return Loss	12.0	16.0		dB	500MHz
Noise Figure		2.8	3.8	dB	500 MHz
Device Operating Voltage	4.5	4.9	5.3	V	
Device Operating Current	82	90	98	mA	
Thermal Resistance		43		°C/W	junction to lead

Test Conditions: V_S=8V, I_D=90 mA Typ., T_L=25 °C. OIP₃ Tone Spacing=1MHz, P_{OUT} per tone=0dBm, R_{BIAS}=33Ω. Data with Application Circuit.

SBF5089Z



Absolute Maximum Ratings

		1
Parameter	Rating	Unit
Device Current (I _D)	150	mA
Device Voltage (V _D)	6	V
RF Input Power	+19	dBm
Max Operating Dissipated Power	0.8	W
Junction Temp (T _J)	+150	°C
Operating Temp Range (T_L)	-40 to +85	°C
Storage Temp	+150	°C
ESD Rating - Human Body Model (HBM)	Class 1C	
Moisture Sensitivity Level	MSL 2	

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression: $I_DV_D < (T_J-T_L)/R_{TH}$, j-l and $T_L = T_{LEAD}$

Typical RF Performance at Key Operating Frequencies



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical perfor-mance or functional operation of the device under Absolute Maximum Rating condi-tions is not implied.

RoHS status based on EU Directive 2002/95/EC (at time of this document revision).

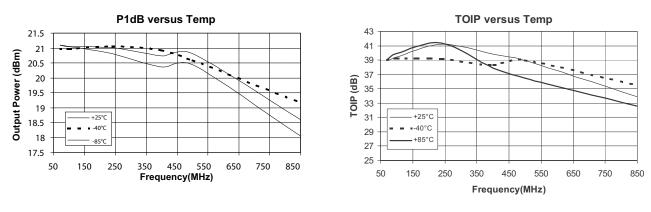
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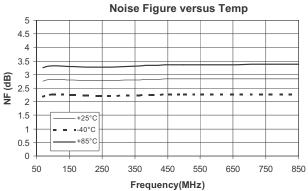
Parameter	Unit	70MHz	100MHz	240MHz	400MHz	500 MHz	850MHz
Small Signal Gain	dB	20.5	20.4	20.1	19.8	19.5	18.2
Output Third Order Intercept Point	dBm	39	39	41	39.5	39	34
Output Power at 1dB Compression	dBm	21.0	21.0	21.0	20.7	20.8	18.6
Input Return Loss	dB	19.4	19.9	20.1	20.9	22.0	26.8
Output Return Loss	dB	17.2	15.8	18.6	24.0	37.5	15.5
Reverse Isolation	dB	25.2	22.4	22.3	22.3	22.3	22.4
Noise Figure	dB	2.7	2.8	2.7	2.8	2.8	2.8

Test Conditions: $V_S=8V$, $I_D=90$ mA Typ., OIP₃ Tone Spacing=1MHz, P_{OUT} per tone=0dBm. $T_L=25$ °C, $R_{BIAS}=33\Omega$, $Z_S=Z_L=50\Omega$, App circuit.





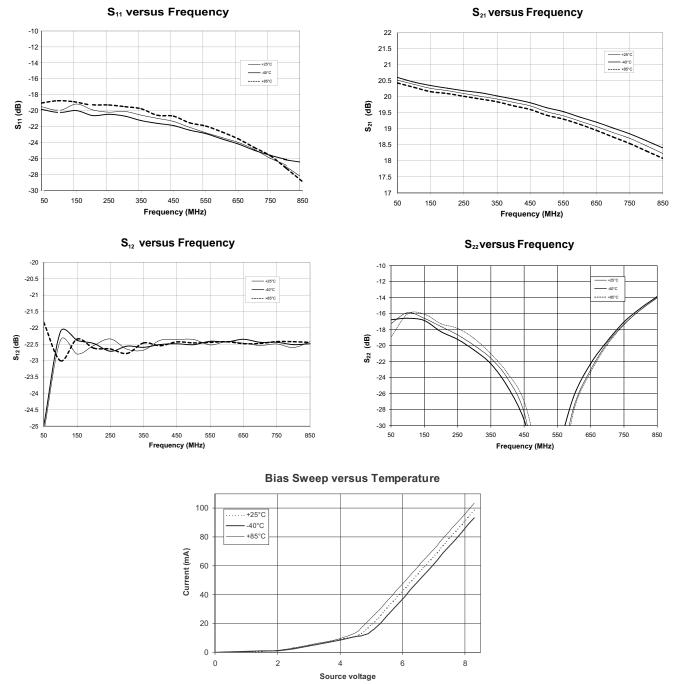








Test Conditions: V_S =8V, R_{BIAS} =33 Ω , I_D =90mA, T=+25°C,

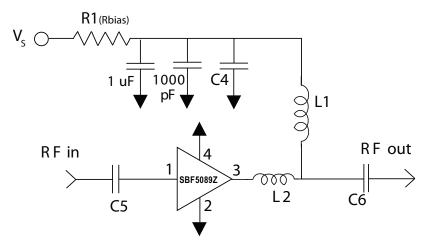


Note: Output Return Loss can be improved at low end of band with L1 selection.





Application Circuit Schematic



Application Circuit Element Values

Reference Designator	70MHz	100 MHz	240 MHz	500MHz	850 MHz
C5 & C6	1uF	1000pF	1000 pF	220pF	100 pF
C4	1uF	100 pF	100 pF	100 pF	68pF
L1	6.8uH	1.2uH	1.2 uH	68nH	33nH
L2	6.8nH	12nH	12nH	6.8nH	6.8nH

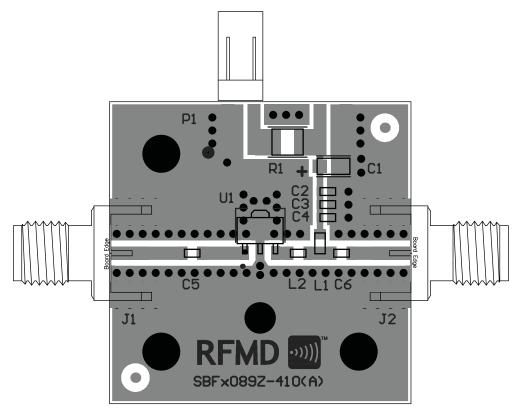
Recommended Bias Resistance for $I_D = 90 \text{ mA}$

Supply Voltage (V_S) (Volts)	7.5	8	10	12
Bias Resistance (Ω)	27	33	55	77

Note = R_{BIAS} provides DC bias stability over temperature.



Evaluation Board Layout



Mounting Instructions

1. Note: For broadband RF unconditional stability do not put GND vias under the exposed backside GND paddle.

2. Solder the copper pad on the backside of the device package to the ground plane.

3. USe a large ground pad area with many plated through-holes as shown.

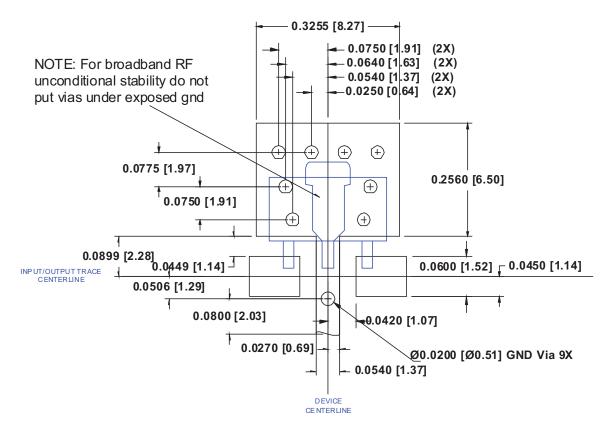
4. We recommend 1 or 2 ounce copper. Measurement for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of opera- tion.
2, 4	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin therefore a DC-blocking capacitor is necessary for proper operation.



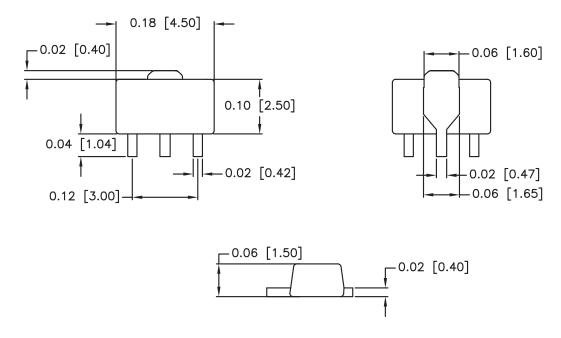


Suggested Pad Layout



Package Drawing

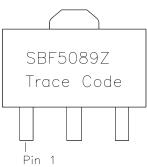
Dimensions in inches (millimeters) Refer to drawing posted at www.rfmd.com for tolerances.







Part Identification



Ordering Information

Ordering Code	Description
SBF5089Z	7" Reel with 1000 pieces
SBF5089ZSQ	Sample Bag with 25 pieces
SBF5089ZSR	7" Reel with 100 pieces
SBF5089ZPCK1	100 MHz to 240 MHz, 8V Operation PCBA with 5-piece Sample Bag

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Qorvo: SBF5089Z