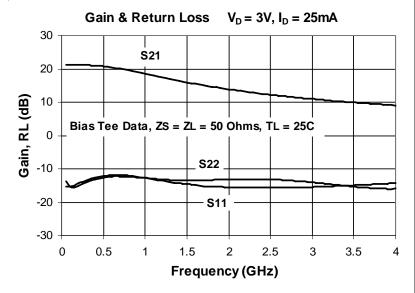


Sirenza Microdevices<sup>7</sup> SGC-2486Z is a high performance SiGe HBT MMIC amplifier utilizing a Darlington configuration with a patented active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 3V supply, the SGC-2486Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SGC-2486Z is designed for high linearity 3V gain block applications that require small size and minimal external components. It is internally matched to 50 ohms.



# SGC-2486Z

50-4000 MHz Active Bias Silicon Germanium Cascadable Gain Block





# **Product Features**

- Single Fixed 3V Supply
- No Dropping Resistor Required
- Patented Self-Bias Circuitry
- P1dB = 10.8 dBm at 1950 MHz
- OIP3 = 23.5 dBm at 1950 MHz
- Robust 1000V ESD, Class 1C HBM

## Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS, WCDMA
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameters	Units	Frequency	Min.	Тур.	Max.
			850 MHz	18.4	19.9	21.4
G	Small Signal Gain	dB	1950 MHz	13.1	14.6	16.1
			2400 MHz		12.3	
			850 MHz		10.5	
P <sub>1dB</sub>	Output Power at 1dB Compression	dBm	1950 MHz	9.8	10.8	
			2400 MHz		10.1	
			850 MHz		23.0	
OIP <sub>3</sub>	Output Third Order Intercept Point	dBm	1950 MHz	21.5	23.5	
			2400 MHz		25.0	
IRL	Input Return Loss	dB	1950 MHz	11.0	15.0	
ORL	Output Return Loss	dB	1950 MHz	9.5	13.5	
NF	Noise Figure	dB	1930 MHz		3.3	4.3
V <sub>D</sub>	Device Operating Voltage	V			3	
I <sub>D</sub>	Device Operating Current	mA		21	25	29
Rth, j-l	Thermal Resistance (junction to lead)	°C/W			205	
Test Condition	<b>ns:</b> $V_D = 3.0V$ $I_D = 25mA Typ.$ $T_L = 25mA Typ.$	5°C	OIP <sub>3</sub> Tone	e Spacing =	1MHz	
Bias Tee Data $Z_S = Z_L = 50 \text{ Ohms}$ Pout per tone = -5 dBm						

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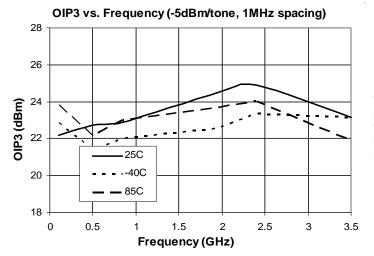
303 S. Technology Ct. Broomfield, CO 80021

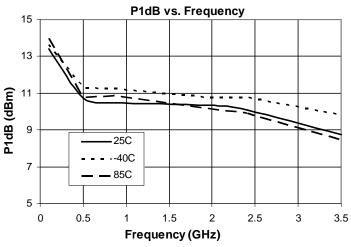
EDS-104974 Rev C



Symbol	Parameter	Unit	Frequency (MHz)					
Symbol	Farameter	Onit	100	500	850	1950	2400	3500
G	Small Signal Gain	dB	21.5	21.0	19.9	14.6	12.3	9.6
OIP <sub>3</sub>	Output Third Order Intercept Point	dBm	22.0	22.5	23.0	23.5	25.0	23.0
$P_{1dB}$	Output Power at 1dB Compression	dBm	13.4	10.7	10.5	10.8	10.1	8.8
IRL	Input Return Loss	dB	14.5	11.5	12.5	15.0	16.5	15.0
ORL	Output Return Loss	dB	14.5	12.0	12.5	13.5	14.0	14.0
<b>S</b> <sub>12</sub>	Reverse Isolation	dB	23.5	25.0	24.5	20.0	19.0	17.5
NF	Noise Figure	dB	2.8	2.8	3.1	3.3	3.6	4.4

Typical Performance with Bias Tee,  $V_{\rm D}$  = 3V,  $I_{\rm D}$  = 25mA





Absolute Maximu	m Ratings	Reliability & Qualification Information			
Parameter Absolute Limit		Parameter	Rating		
Max Device Current (I <sub>CE</sub> )	55 mA	ESD Rating - Human Body Model (HBM)	Class 1C		
Max Device Voltage (V <sub>CE</sub> )	4.5 V	Moisture Sensitivity Level MSL			
Max. RF Input Power* (See Note)	+18 dBm	This product qualification report can be downloaded at			
Max. Junction Temp. (T <sub>J</sub> )	+150°C	www.sirenza.com			
Operating Temp. Range (T <sub>L</sub> )	-40°C to +85°C				
Max. Storage Temp.	+150°C	Caution: ESD sensitive			
<b>Note:</b> Load condition, $Z_L = 50$ Ohms			Appropriate precautions in handling, packaging and testing devices must be observed.		
Operation of this device beyond any one permanent damage. For reliable continu					

voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression:  $T_L = T_{LEAD}$ 

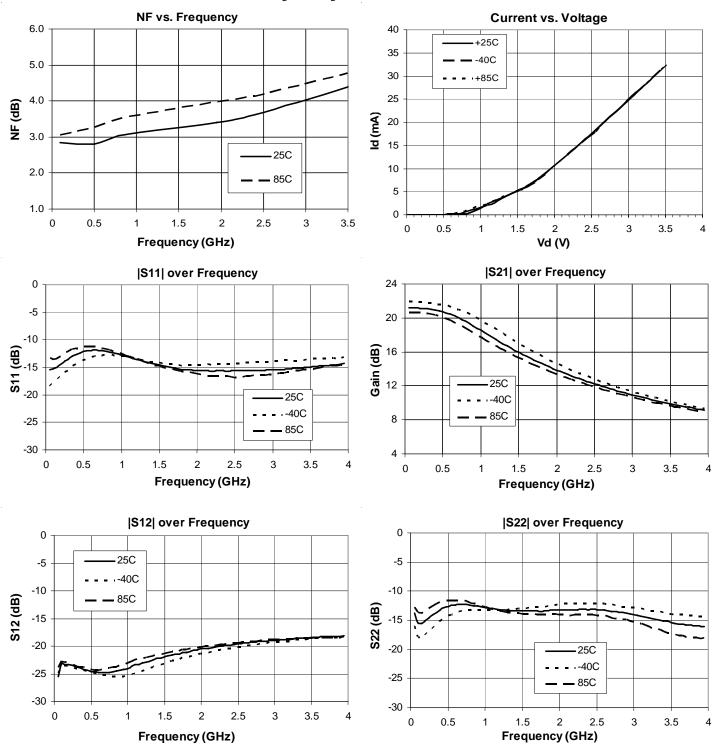


Phone: (800) SMI-MMIC



SGC-2486Z 0.05-4.0 GHz Cascadeable MMIC Amplifier

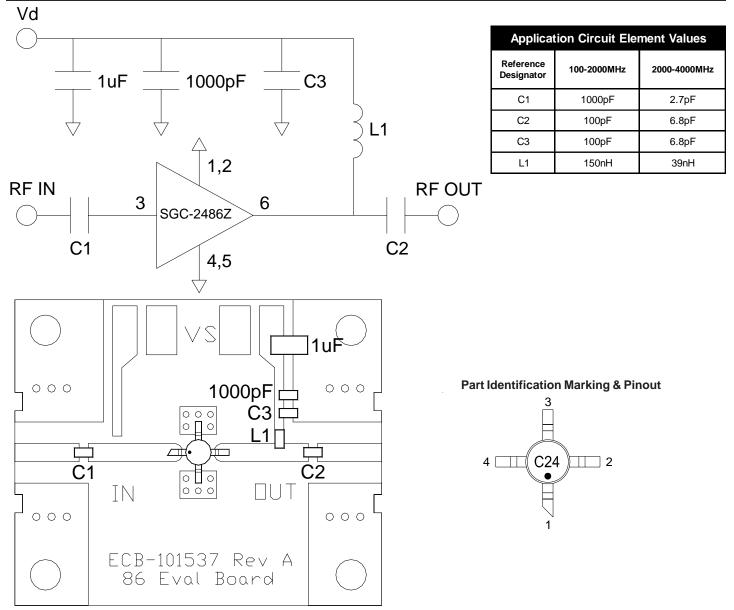
Typical Performance with Bias Tee,  $V_{D} = 3V$ ,  $I_{D} = 25mA$ 



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#### SGC-2486Z 0.05-4.0 GHz Cascadeable MMIC Amplifier



Pin #	Function	Description	Part / Evaluation Board Ordering Information				
1		RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation	Part Number	Description	Reel Size	Devices / Reel	
2,4	GND	Connection to ground. Use via holes as close to the device ground leads as possible to reduce ground inductance and achieve optimum RF performance	SGC-2486Z	Lead Free, RoHs Compliant	13"	3000	
			SGC-2486Z-EVB1	100-2000 MHz Evaluation Board	N/A	N/A	
3	DC BIAS	RF output and bias pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	SGC-2486Z-EVB2	2000-4000 MHz Evaluation Board	N/A	N/A	

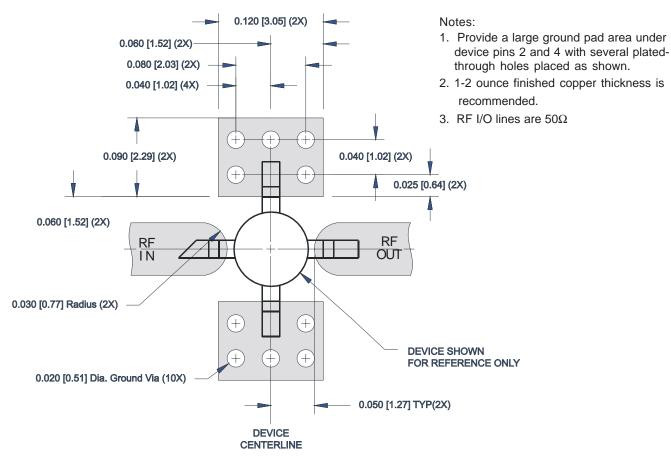


#### SGC-2486Z 0.05-4.0 GHz Cascadeable MMIC Amplifier

## 86 PCB Pad Layout

Dimensions in inches [millimeters]

EDS-104974 Rev C



## **86 Nominal Package Dimensions**

**Dimensions in inches [millimeters]** A link to the 86 package outline drawing with full dimensions and tolerances may be found on the product web page at www.sirenza.com.

