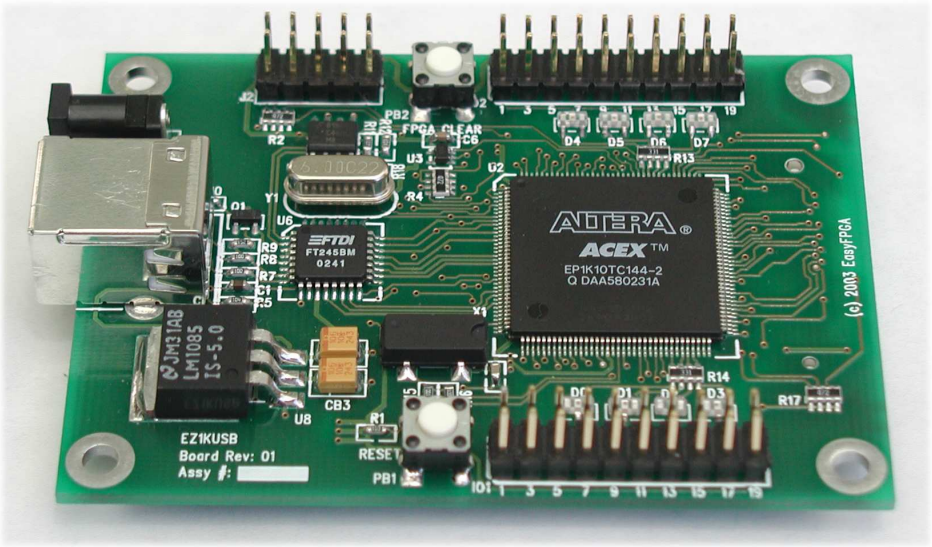
EZ1KUSB—Altera ACEX Development Board

FPGA Development board with USB interface



Features

- Connects to the host PC via USB
- USB data transfer up to 1MB per second
- Easy configuration via USB or JTAG
- High Density FPGA (10,000 to 50,000 gates, up to 40,960 RAM bits)
- On-board clock oscillator and reset circuitry
- 58 I/O pins
- 3 dedicated input pins
- Internal Clock Output/ External Clock Input
- External Reset Input
- Eight LEDs
- Reset Push-button, FPGA Clear Push-button
- Royalty free USB driver (Win. 98/Me/2000/XP)
- No specific USB firmware programming required
- VB6/VB.NET sample applications
- VHDL code samples

Applications

- Rapid prototyping and development of Altera ACEX design
- IP development and testing
- Telecommunications
- Digital Signal Processing
- Data acquisition and control
- Signal generation
- USB Digital Camera Interface
- USB to synchronous bus bridge

Featured FPGAs

- Altera ACEX EP1K10TC144-3
  10,000 typical gates, 576 LEs, 12,288 RAM bits
- Altera ACEX EP1K30TC144-3
  30,000 typical gates, 1728 LEs, 24,576 RAM bits
- Altera ACEX EP1K50TC144-3
  50,000 typical gates, 2,880 LEs, 40,960 RAM bits

System Specification

- Physical dimensions: 2.9” x 2.2”
- DC Input: 6V, 250 mA, 1.3 mm power jack
- 2x 20 pin 0.100” (2.54 mm) pitch header
- 1x high density 50 pin 0.8 mm pitch header
- USB (type “B”) connector

Cables And Accessories

- DC Power Supply
- USB A->B cable
- Documentation
- USB Drivers
- VB6/VB.NET sample applications
- VHDL code samples

Warranty

- One year - parts, labor, technical support
- Free software upgrades

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