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PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>R</sub>	100 V				
$V_F$ at $I_F$	0.67 V				
I <sub>RM</sub> max.	11 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
E <sub>AS</sub>	7.50 mJ				
Package	TO-220AB 3L				
Circuit configuration	Common cathode				

## FEATURES

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	- CHARACTERISTICS VALUES U					
I <sub>F(AV)</sub>	Rectangular waveform	40	А			
V <sub>RRM</sub>		100	V			
I <sub>FSM</sub>	$t_p = 5 \ \mu s$ sine	850	А			
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.67	V			
TJ	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-43CTQ100-M3	UNITS		
Maximum DC reverse voltage	VR	100	M		
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average forward per leg	$I_{F(AV)}$ 50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		20				
current, see fig. 5 per device	I <sub>F(AV)</sub>	$30\%$ unity cycle at $T_{\rm C} = 135\%$ C, rectangular wavelonn		40	A		
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse Following any rated load condition and		850			
non-repetitive surge current per leg, see fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	with rated V <sub>RRM</sub> applied	275			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_{J} = 25 \text{ °C}, I_{AS} = 0.50 \text{ A}, L = 60 \text{ mH}$		7.50	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.50	А		

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HALOGEN

FREE



ELECTR	ICAL S	PECIFIC	ATIONS

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		20 A	T.I = 25 °C	0.81		
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 C	0.98	V	
	V FM (1)	20 A	T 105 %C	0.67	V	
		40 A	T <sub>J</sub> = 125 °C	0.81		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V DetectV	1	mA	
See fig. 2		T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	11		
Threshold voltage	V <sub>F(TO)</sub>	T T mavimum		0.71	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J maximum$	·	0.43	mΩ	
Maximum junction capacitance per leg	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 $^{\circ}\text{C}$		1480	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs		

### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storag temperature range	e	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		P		2.0		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50		
Approvimeto weight				2	g	
Approximate weight				0.07	oz.	
Manualiantana	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style 3L TO-220AB	43CT	Q100	



# VS-43CTQ100-M3

## **Vishay Semiconductors**

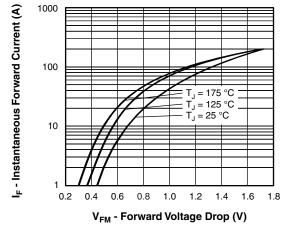
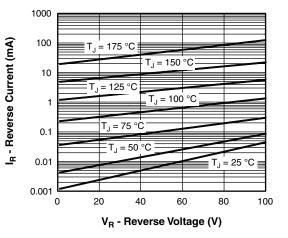
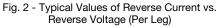


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





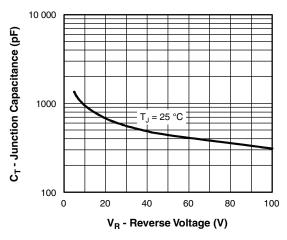
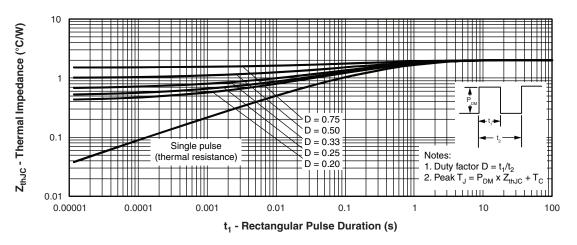


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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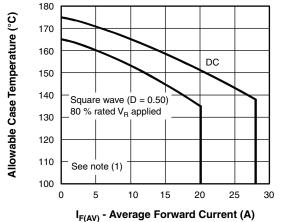
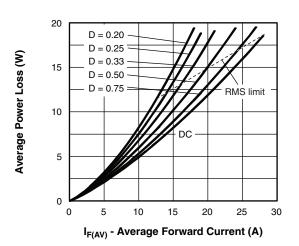
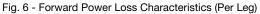
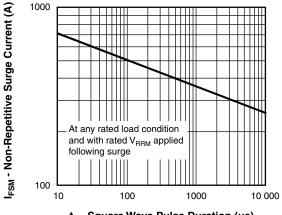


Fig. 5 - Maximum Allowable Case Temperature vs. Average









tp - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

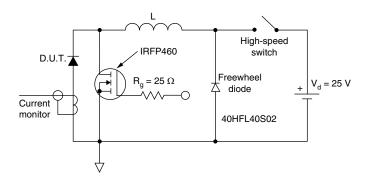


Fig. 8 - Unclamped Inductive Test Circuit

### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \ \mathsf{-D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$ 

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VS-43CTQ100-M3

## Vishay Semiconductors



Device code	VS-	43	С	т	Q	100	-M3
	1	2	3	4	5	6	7
1	-	-		onductor	s produ	ct	
3	-	Current rating (40 A) Circuit configuration					
4	-	C = co Packag	mmon c je	athode			
5	-	T = TC Schottl	-220 ky "Q" se	eries			
6	-	Voltage rating (100 = 100 V)					
7	-		nmental halogen	digit -free, R	oHS-cor	mpliant,	and ter

ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-43CTQ100-M3	50	Antistatic plastic tubes				

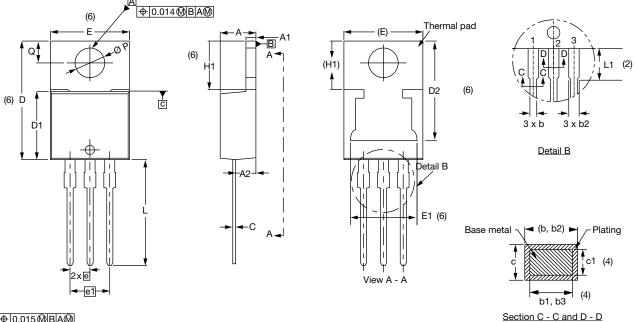
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96154			
Part marking information	www.vishay.com/doc?95028			
SPICE model	www.vishay.com/doc?95065			





# **TO-220AB 3L**

### **DIMENSIONS** in millimeters and inches



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SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

SYMBOL	MILLINETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NUTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØP	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

INCHES

#### Notes

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

<sup>(4)</sup> Dimension b1, b3, and c1 apply to base metal only

<sup>(5)</sup> Controlling dimensions: inches

- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- <sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

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Conforms to JEDEC<sup>®</sup> outline TO-220AB

MILLIMETEDS

 $<sup>^{(1)}\,</sup>$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>&</sup>lt;sup>(3)</sup> Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



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