SGA6389Z

RFMD + TriQuint = Qorvo

DC to 4500 MHz, CASCADABLE SiGe HBT MMIC AMPLIFIER

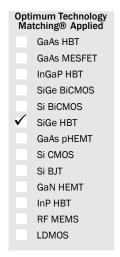
Package: SOT-89

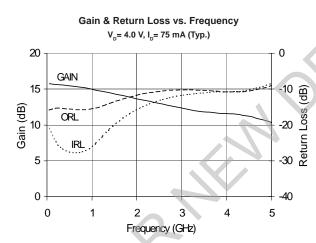




Product Description

The SGA6389Z is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one-micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.





Features

- Broadband Operation: DC to 4500MHz
- Cascadable 50Ω
- Operates from Single Supply
- Low Thermal Resistance Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Specification		Unit	Condition		
Min.	Тур.	Max.	Oilit	Condition	
14.1	15.5	17.3	dB	850MHz	
	14.0		dB	1950MHz	
	13.3		dB	2400MHz	
	20.2		dBm	850MHz	
	18.9		dBm	1950MHz	
	35.2		dBm	850MHz	
	32.6		dBm	1950MHz	
	4500		MHz	>10dB	
	16.0		dB	1950MHz	
	11.9		dB	1950MHz	
	4.2		dB	1950MHz	
4.6	4.9	5.4	V		
72	80	88	mA		
	97		°C/W		
	4.6 72	Min. Typ. 14.1 15.5 14.0 13.3 20.2 18.9 35.2 32.6 4500 16.0 11.9 4.2 4.6 4.9 72 80 97	Min. Typ. Max. 14.1 15.5 17.3 14.0 13.3 20.2 18.9 35.2 32.6 4500 16.0 11.9 4.2 4.6 4.9 5.4 72 80 88 97	Min. Typ. Max. 14.1 15.5 17.3 dB 14.0 dB dB 13.3 dB 20.2 dBm 18.9 dBm dBm 35.2 dBm 32.6 dBm 4500 MHz dB 4500 dB 11.9 dB 4.2 dB 4.6 4.9 5.4 V 72 80 88 mA 97 °C/W	

Test Conditions: V_S =8V, I_D =80 mA Typ., OIP₃ Tone Spacing=1MHz, P_{OLIT} per tone=0dBm, R_{BIAS} =39 Ω , T_L =25 $^{\circ}$ C, Z_S = Z_L =50 Ω



Absolute Maximum Ratings

Parameter	Rating	Unit
Max Device Current (I _D)	160	mA
Max Device Voltage (V _D)	7	V
Max RF Input Power	+18	dBm
Max Junction Temp (T _J)	+150	°C
Operating Temp Range (T _L)	-40 to +85	°C
Max Storage Temp	+150	°C
Moisture Sensitivity Level	MSL 2	

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression: $I_DV_D\!<\!(T_J\!-\!T_L)/\,R_{TH},j\!-\!I$



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

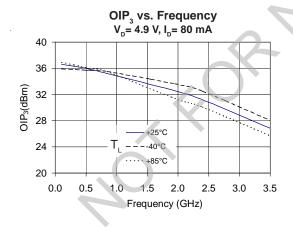
RoHS status based on EUDirective 2002/95/EC (at time of this document revision).

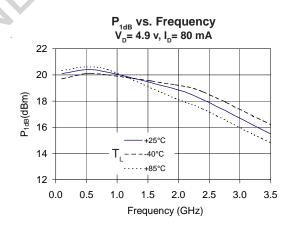
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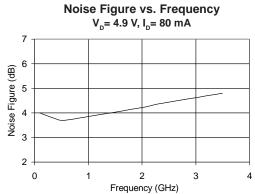
Typical Performance at Key Operating Frequencies

Parameter	Unit	100	500	850	1950	2400	3500
		MHz	MHz	MHz	MHz	MHz	MHz
Small Signal Gain	dB	15.7	15.6	15.5	14.0	13.3	12.0
Output Third Order Intercept Point	dBm	36.6	36.0	35.2	32.6	31.2	26.8
Output Power at 1dB Compression	dBm	20.1	20.4	20.2	18.9	18.1	15.5
Input Return Loss	dB	22.3	27.5	27.4	16.0	13.7	11.0
Output Return Loss	dB	14.7	14.5	14.8	11.9	10.8	10.4
Reverse Isolation	dB	20.4	20.2	20.3	20.1	19.7	18.2
Noise Figure	dB	4.0	3.7	3.8	4.2	4.4	4.8

 $\textbf{Test Conditions: V}_S = \textbf{8V, I}_D = \textbf{80 mA Typ., OIP}_3 \textbf{ Tone Spacing} = \textbf{1MHz, P}_{OUT} \textbf{ per tone} = \textbf{0dBm, R}_{BIAS} = \textbf{39} \Omega, \textbf{T}_L = 25\,^{\circ}\text{C}, \textbf{Z}_S = \textbf{Z}_L = 50 \Omega$

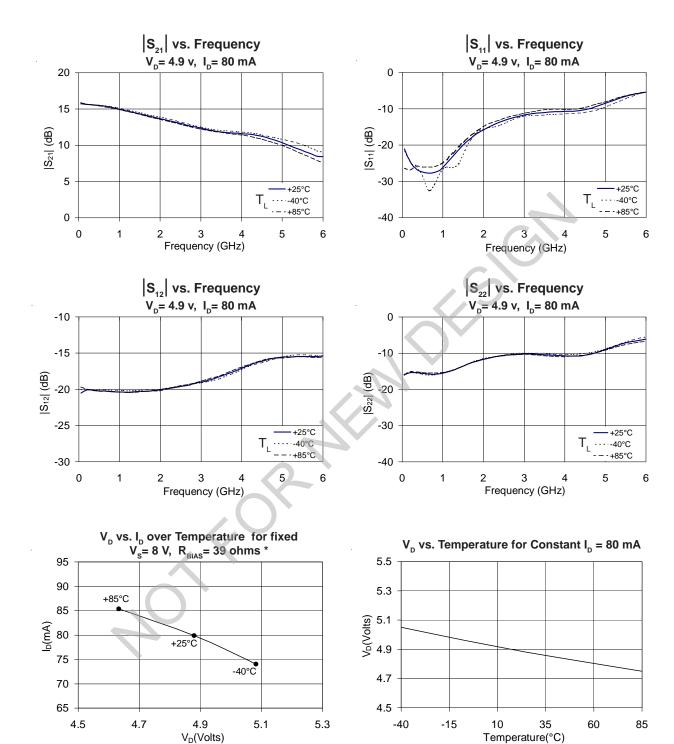










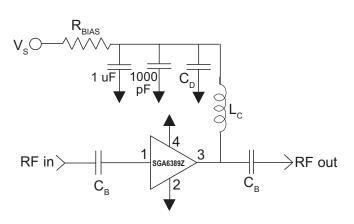


 $^{^{*}}$ Note: In the applications circuit on page 4, $R_{\scriptsize BIAS}$ compensates for voltage and current variation over temperature.



Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefor a DC-blocking capacitor is necessary for proper operation.

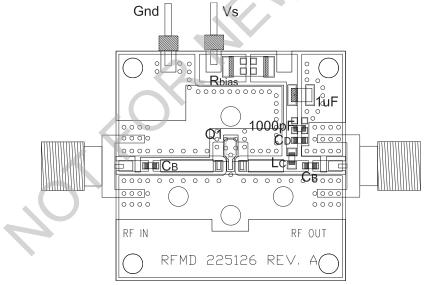
Application Schematic



Reference	Frequency (Mhz)						
Designator	500	850	1950	2400	3500		
C _B	220 pF	100 pF	68 pF	56 pF	39 pF		
C _D	100 pF	68 pF	22 pF	22 pF	15 pF		
L _c	68 nH	33 nH	22 nH	18 nH	15 nH		

Recommended Bias Resistor Values for $I_D=80 mA$ $R_{BIAS}=(V_S-V_D)/I_D$					
Supply Voltage(V _s)	6 V	8 V	10 V	12 V	
R _{BIAS} 13 Ω 39 Ω 62 Ω 91 Ω					
Note: R _{BIAS} provides DC bias stability over temperature.					

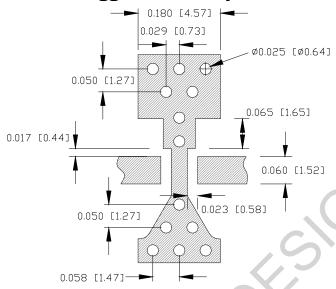
Evaluation Board Layout



Mounting Instructions:

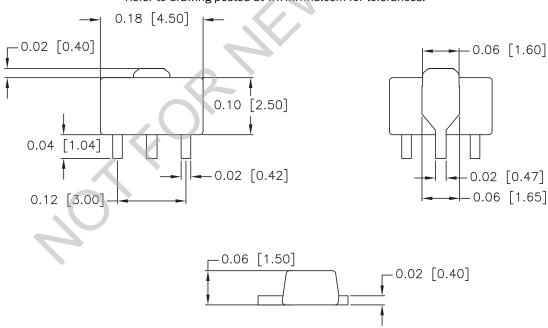
- 1. Solder the copper pad on the backside of the device package to the ground plane.
- 2. Use a large ground pad area with many plated through-holes as shown.
- 3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31mil thick FR-4 board with 1 ounce copper on both sides.

Suggested Pad Layout



Package Drawing

Dimensions in inches (millimeters)
Refer to drawing posted at www.rfmd.com for tolerances.





Part Identification



Ordering Information

Ordering Code	Description
SGA6389Z	13" Reel with 3000 pieces
SGA6389ZSQ	Sample bag with 25 pieces
SGA6389ZSR	7" Reel with 100 pieces
SGA6389ZPCK1	850MHz, 8V Operation PCBA with 5-piece sample bag

Mouser Electronics

Authorized Distributor

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Qorvo: SGA6389Z